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PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Frank O'Mahony et al.

Examiner: Ahmed Sefer

Serial No.: 09/893023

Group Art Unit: 2826

Filed: June 27, 2001

Docket No.: 884.405US1

Title: LOW LOSS INTERCONNECT STRUCTURE FOR USE IN MICROELECTRONIC CIRCUITS

**AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111**

Commissioner for Patents  
Washington, D.C. 20231

Applicant has reviewed the Office Action mailed on January 3, 2003. Please amend the above-identified patent application as follows.

*This response is accompanied by a Petition, as well as the appropriate fee, to obtain a one-month extension of the period for responding to the Office action, thereby moving the deadline for response from April 3, 2003 to May 3, 2003.*

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